

产品概览

AR0237: CMOS 图像传感器, 2.1 MP, 1/2.7"

欲看完整文档, 请参阅数据表。

AR0237 是一款 1/2.7 英寸 CMOS 数字图像传感器, 带有 1928 (H) × 1088 (V) 有效像素阵列。它能在线性或高动态范围模式下捕捉图像, 且带有卷帘快门读取, 其中包含了复杂的摄像头功能, 如像素内装仓、开窗以及视频和单帧模式。它适用于低光度和高动态范围场景性能, 可通过一个简单的两线串行接口进行编程。AR0237 可产生非常清晰、锐利的数字图像, 并且能够同时捕捉连续视频和单帧图像, 适用于监控和高清视频等多种应用。

特性

- Superior low-light performance
- DR-PIX (TM) technology with Dual Conversion Gain
- Full HD support at up to 1080p 60 fps for superior video performance
- Linear or high dynamic range capture
- On-chip phase-locked loop (PLL) oscillator
- Supports line interleaved T1/T2 readout to enable HDR processing in ISP chip
- Support for external mechanical shutter
- Integrated position-based color and lens shading correction
- Slave mode for precise frame-rate control
- Stereo/3D camera support

For more features, see the data sheet

应用

- Video surveillance
- 1080p60 (Surveillance) video applications
- High dynamic range imaging

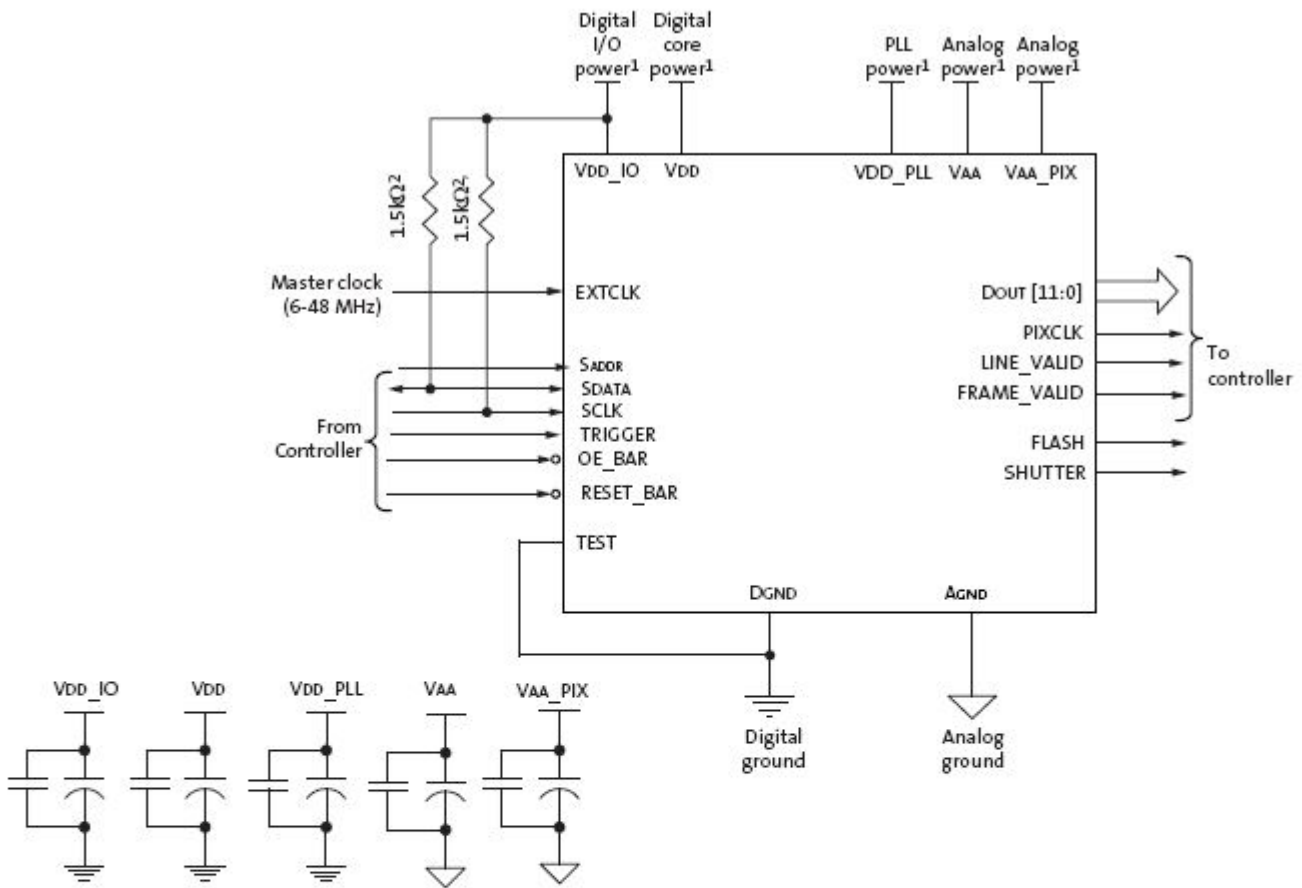
终端产品

- Surveillance camera

器件电气规格

产品	Pricing (\$/Unit)	Compliance	Status	Type	Megapixels	Frame Rate (fps)	Optical Format	Shutter Type	Pixel Size (µm)	Output Interface	Color	Package Type
AR0237CSSC00SHRA0-DR		Pb-free Halide free	Active	CMOS	2.1	60	1/2.7 inch	Electronic Rolling and Global Reset Release	3.0 x 3.0	HiSPi™	Bayer Color	PLCC-48
AR0237CSSC00SPRA0-DR		Pb-free Halide free	Active	CMOS	2.1	60	1/2.7 inch	Electronic Rolling and Global Reset Release	3.0 x 3.0	Parallel	Bayer Color	PLCC-48
AR0237CSSC12SHRA0-DR		Pb-free Halide free	Active	CMOS	2.1	60	1/2.7 inch	Electronic Rolling and Global Reset Release	3.0 x 3.0	HiSPi™	Bayer Color	PLCC-48
AR0237CSSC12SPRA0-DR		Pb-free Halide free	Active	CMOS	2.1	60	1/2.7 inch	Electronic Rolling and Global Reset Release	3.0 x 3.0	Parallel	Bayer Color	PLCC-48

应用框图



- Notes:
1. All power supplies must be adequately decoupled.
 2. ON Semiconductor recommends a resistor value of 1.5kΩ, but a greater value may be used for slower two-wire speed.
 3. The serial interface output pads and VDDSLVS can be left unconnected if the parallel output interface is used.
 4. ON Semiconductor recommends that 0.1μF and 10μF decoupling capacitors for each power supply are mounted as close as possible to the pad. Actual values and results may vary depending on layout and design considerations. Refer to the AR0237 demo headboard schematics for circuit recommendations.
 5. ON Semiconductor recommends that analog power planes are placed in a manner such that coupling with the digital power planes is minimized.
 6. I/O signals voltage must be configured to match VDD_IO voltage to minimize any leakage currents.
 7. The EXTCLK input is limited to 6-48 MHz.

欲了解更多信息，请联系您当地的销售支援 www.onsemi.cn。

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